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From: W. Daniel Swayze, Jr.
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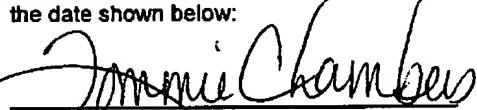
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FACSIMILE COVER SHEET

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| <input type="checkbox"/> DIVISIONAL APPN | <input checked="" type="checkbox"/> LETTER TO EXAMINER |
| NAME OF INVENTOR(S): Chambers | |
| TITLE OF INVENTION: CMOS DC OFFSET CORRECTION CIRCUIT WITH PROGRAMMABLE HIGH-PASS TRANSFER FUNCTION | |
| TI FILE NO.: TI-30883 | DEPOSIT ACCT. NO.: 20-0668 |
| FAXED: 11/17/2005 DUE: ATTY/SECY: wds/nic | |
| RECEIPT DATE & SERIAL NO.: Serial No.: 09/902,051 Filing Date: 7/10/2001 | |

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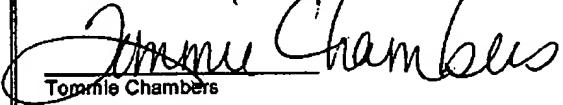
Applicant: Chambers Docket No: TI-30883
Serial No: 09/902,051 Examiner: Davidson, Dan
Filed: 07/10/01 Art Unit: 2651
For: CMOS DC OFFSET CORRECTION CIRCUIT WITH PROGRAMMABLE HIGH-PASS TRANSFER FUNCTION

LETTER TO THE EXAMINER

Assistant Commissioner For Patents
Washington, DC 20231

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Tommie Chambers

Dear Sir:

Pursuant to the telephone conversation between the Examiner and the Applicants representative, Applicants appreciate the indication from the Examiner that the Abandonment of the above identified application will be withdrawn without further action by Applicant.

Respectfully submitted,


W. Daniel Swayze, Jr.
Attorney for Applicant
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